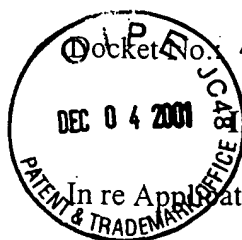


Receipt #3

PATENT



Docket No. 49657-911

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Yoshihito OCHI, et al.

Serial No.: 09/764,137

Filed: January 19, 2001

For: CIRCUIT SIMULATING APPARATUS PERFORMING SIMULATION WITH
UNNECESSARY CIRCUIT DISCONNECTED

Group Art Unit: 2123

Examiner:

Second Request
December 4, 2001

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents
Washington, DC 20231

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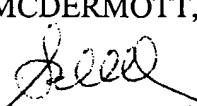
Sir:

Attached is a copy of the Filing Receipt received from the U.S. Patent and Trademark Office in the above-referenced application. It is noted that the Title is incorrect on the Filing Receipt. Attached is a copy of the Declaration and Power of Attorney, which evidences that the correct title of the application should read: **Circuit simulating apparatus performing simulation with unnecessary circuit disconnected.**

It is requested that a corrected Filing Receipt be issued.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

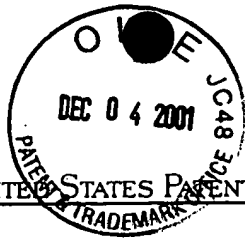

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UNITED STATES PATENT AND TRADEMARK OFFICE

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/764,137	01/19/2001	2123	710	49657-911	10	20	3

CONFIRMATION NO. 8210

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OC000000005827304

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Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the PTO processes the reply to the Notice, the PTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Yoshihito Ochi, Hyogo, JAPAN;
Tetsuya Muta, Hyogo, JAPAN;
Yoshiki Nakamura, Hyogo, JAPAN;

Assignment For Published Patent Application

Mitsubishi Denki Kabushiki Kaisha and Mitsubishi Electric System LSI Design Corporation,;

Continuing Data as Claimed by Applicant

Foreign Applications

JAPAN 2000-215558 07/17/2000

If Required, Foreign Filing License Granted 03/05/2001

Projected Publication Date: 01/17/2002

Non-Publication Request: No

Early Publication Request: No

Title

Circuit
Circuit simulating apparatus performing simulation with unnecessary circuit disconnected

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Preliminary Class

703

Data entry by : PETROS, TADELECH

Team : OIPE

Date: 03/05/2001

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